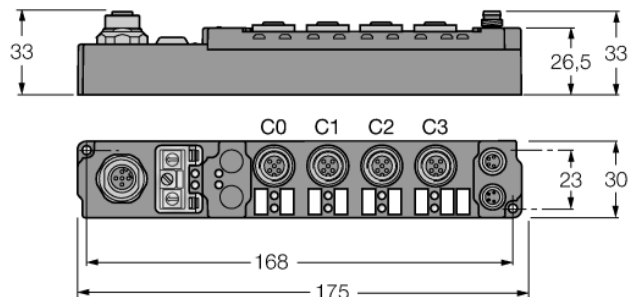


piconet® stand-alone module for PROFIBUS-DP
4 analog inputs for Pt100
SDPB-40A-0009

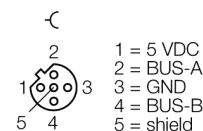
TURCK

Industrial
Automation

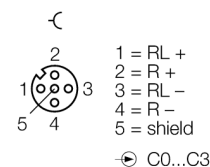


- 4 analogue inputs for Pt100
- Interfejs konfiguracyjny
- Parametryzowane funkcje
- Obsługa za pomocą I/O-ASSISTANT 2
- Direct connection to the fieldbus
- Fibre-glass reinforced housing
- Shock and vibration tested
- Encapsulated module electronics
- Metal connector
- Degree of protection IP67

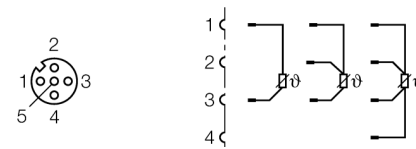
Złącze magistrali M12 × 1



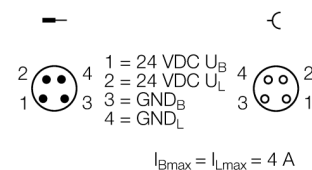
Wejście M12 × 1



Podłączenie - wejścia



Zasilanie M8 × 1



Typ	SDPB-40A-0009
Nr kat.	6824040
Liczba kanałów	4
Napięcie pracy / obciążenia	20...29 VDC
Prąd pracy	≤ 110 mA
Prędkość transmisji sieciowej	9.6 kbps ... 12 Mbps
Adresowanie sieciowe	0 to 99
Interfejs serwisowy	parameterisation via I/O-ASSISTANT
Izolacja elektryczna	Fieldbus to operational voltage
Liczba kanałów	4 analogue inputs Pt100
Izolacja elektryczna	channels to operational voltage
Sensor type	Pt100
Temperature range	-200 to 850 °C (Pt sensors), -60 to 250 °C (Ni sensors)
Mierzony prąd	0.1 °C
Conversion time	250 ms
Relative measuring error	< ±1.0 % of full scale
Input filter	variable
Zasilanie czujników	from operational voltage
Dimensions (W x L x H)	30 x 175 x 26.5mm
Temperatura pracy	0...+55 °C
Temperatura składowania	-25 to 85 °C
Test wibracyjny	as per EN 60068-2-6
Test przeciążeniowy/wstrząsowy	acc. to DIN EN 60068-2-27
Kompatybilność elektromagnetyczna	according to EN 61000-6-2/EN 61000-6-4
Klasa ochrony	IP67
Certyfikaty	CE, cULus

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Dane z odwzorowania procesu

Valid for the setting "Motorola format"

SBn: Status byte channel n
 CBn: Control byte channel n
 Chn D0: channel n,
 least significant data byte
 Chn D1: channel n,
 most significant data byte

	Address	Input data		Output data	
Pre-conditions	Word	High-Byte	Low-Byte	High-Byte	Low-Byte
Compact mapping: Starting with Ch0 D1 in "Low-Byte" word 0 all other bytes follow immediately. Only the user data are mapped (greyed in the table). Complex mapping: Data are mapped with control and status byte.	0	Ch0 D1	SB0	Ch0 D1	CB0
	1	SB1	Ch0 D0	CB1	Ch0 D0
	2	Ch1 D0	Ch1 D1	Ch1 D0	Ch1 D1
	3	Ch2 D1	SB2	Ch2 D1	CB2
	4	SB3	Ch2 D0	CB3	Ch2 D0
	5	Ch3 D0	Ch3 D1	Ch3 D0	Ch3 D1